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		Application Number	10/046,220
		Filing Date	January 16, 2002
		First Named Inventor	James Herbert Kukula
		Art Unit	2185
		Examiner Name	
Sheet	1	of	1
		Attorney Docket Number	06816.0110

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
awp	1	H. Cho, G. D. Hachtel, E. Macii, B. Plessier, and F. Somenzi. "Algorithms for Approximate FSM Traversal." Proc. 30th ACM/IEEE Design Automation Conference, pp. 25-30, 1993.	
awp	2	H. Cho, G. D. Hachtel, E. Macii, M. Poncino, and F. Somenzi. "A Structural Approach to State Space Decomposition for Approximate Reachability Analysis." In Proc. IEEE Intl. Conf. on Computer Design (ICCD), October 10-12, 1994, pp236-239.	
awp	3	H. Cho, G. D. Hachtel, E. Macii, M. Poncino, and F. Somenzi. "Automatic State Space Decomposition for Approximate FSM Traversal Based on Circuit Analysis." IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 15, No. 12, December 1996, pp1451-1464.	
awp	4	H. Cho, G. D. Hachtel, E. Macii, B. Plessier, and F. Somenzi. "Algorithms for Approximate FSM Traversal Based on State Space Decomposition." IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 15, No. 12, December 1996, pp1465-1478.	
awp	5	C. H. Yang and D. L. Dill. "Validation with Guided Search of the State Space." In Proc. of the Design Automation Conf., June 1998, pp599-604.	
awp	6	Govindaraju, G. S. and Dill, D. L. "Verification by Approximate Forward and Backward Reachability." IEEE/ACM ICCAD, pp. 366-370, Nov. 8-12, 1998.	

Examiner Signature	ALBERT W. PALADIN <i>Albert W. Paladin</i>	Date Considered	8-8-05
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